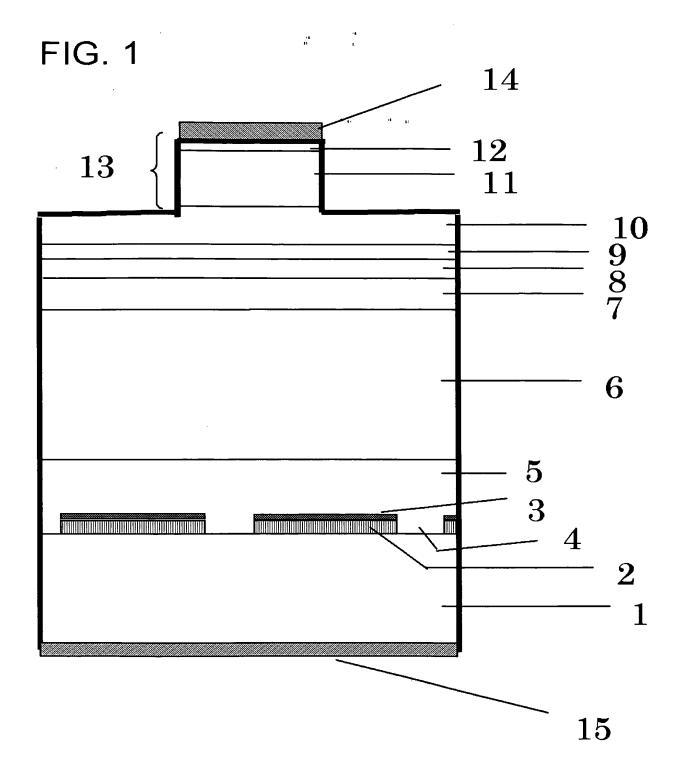
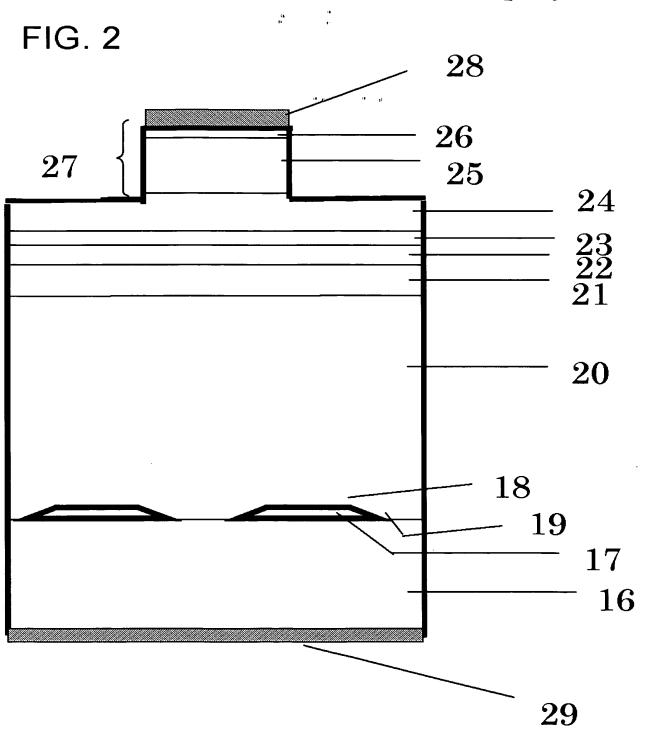
National Stage Entry of PCT/JP2004/012467 "Nitride Semiconductor Substrate and Nitride Semiconductor Device Employing Same" Filed June 3, 2005 Atty: Bernstein; Tel.: 202-293-7060; Ref.: Q88048



National Stage Entry of PCT/JP2004/012467 "Nitride Semiconductor Substrate and Nitride Semiconductor Device Employing Same" Filed June 3, 2005

Atty: Bernstein; Tel.: 202-293-7060; Ref.: Q88048

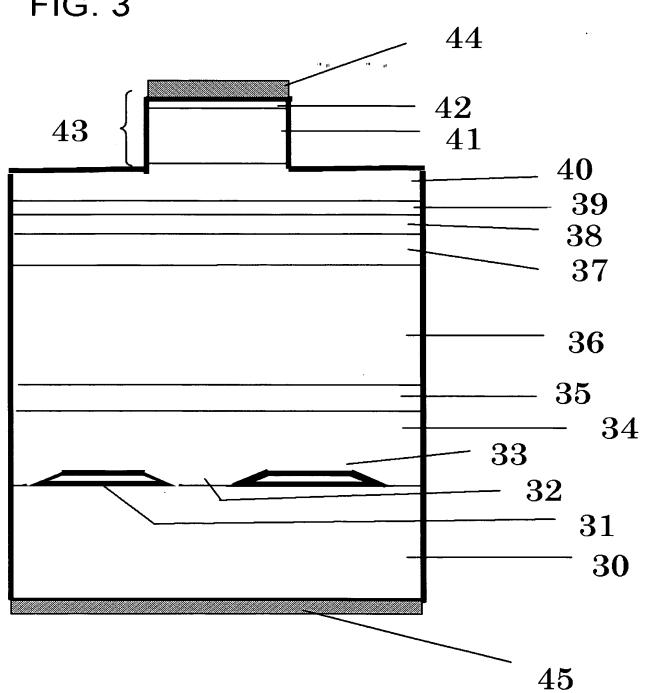


National Stage Entry of PCT/JP2004/012467 "Nitride Semiconductor Substrate and Nitride Semiconductor Device Employing Same" Filed June 3, 2005

Atty: Bernstein; Tel.: 202-293-7060; Ref.: Q88048

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FIG. 3



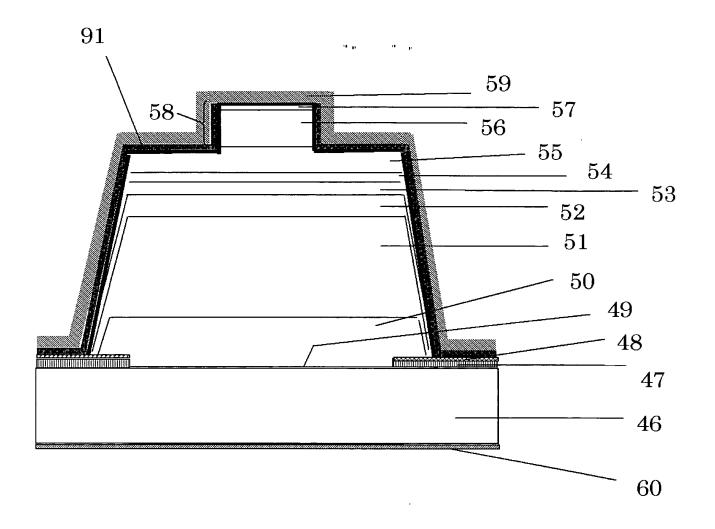
National Stage Entry of PCT/JP2004/012467 "Nitride Semiconductor Substrate and Nitride Semiconductor Device Employing Same"

Filed June 3, 2005 Atty: Bernstein; Tel.: 202-293-7060; Ref.: Q88048

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FIG. 4



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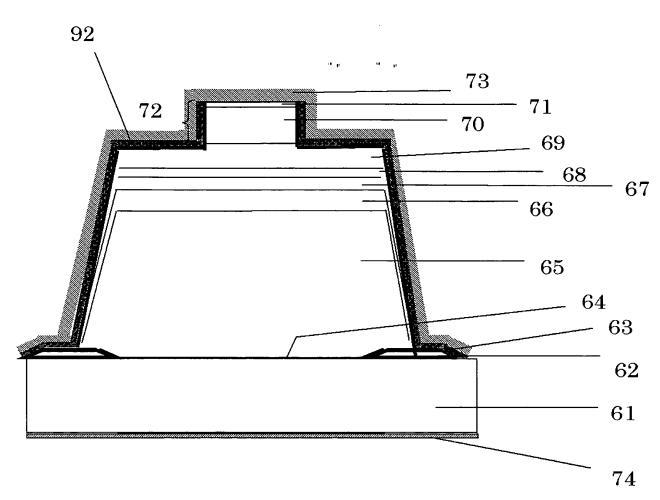
National Stage Entry of PCT/JP2004/012467 "Nitride Semiconductor Substrate and Nitride Semiconductor Device Employing Same" Filed June 3, 2005

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FIG. 5

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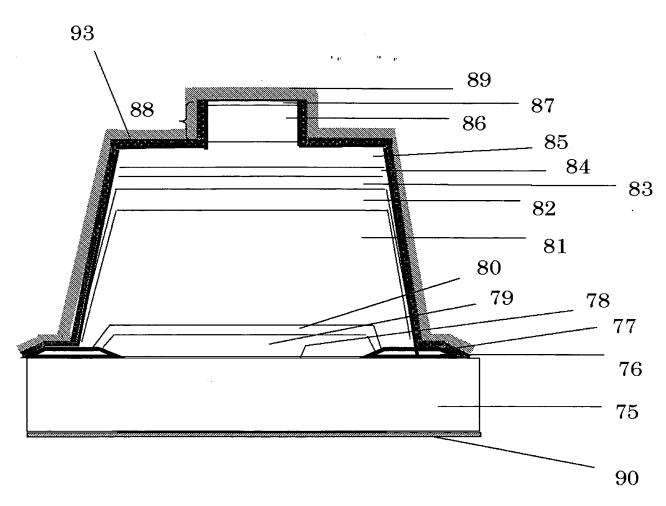
National Stage Entry of PCT/JP2004/012467
"Nitride Semiconductor Substrate and Nitride Semiconductor Device Employing Same"
Filed June 3, 2005

Atty: Bernstein; Tel.: 202-293-7060; Ref.: Q88048

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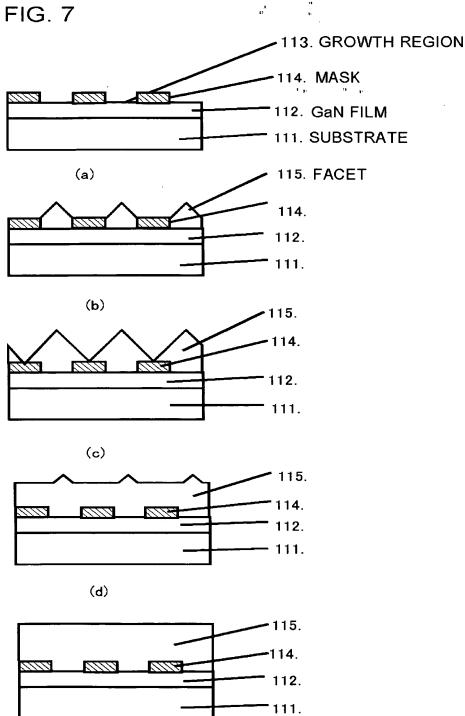
FIG. 6

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FIG. 8

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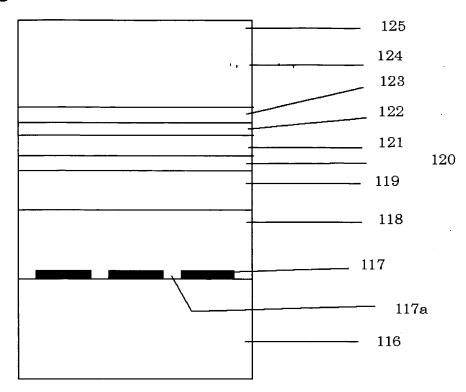
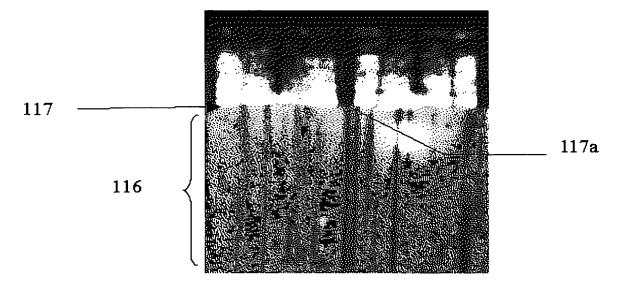


FIG. 9



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FIG. 10

 $\langle 11 \cdot 20 \rangle$  DIRECTION



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 $\langle 1 \text{-} 100 \rangle$  DIRECTION